

Integrated N-PLC SCADA Controller: Solar Micro-inverters, Smart Ballasts



Overview

The *SM2480* is a member of the *SM24xx* family that expands the *SM2400* Narrowband Power Line Communication (N-PLC) modem to a fully integrated Analog Controller with Grid connectivity. It is specifically targeted for applications such as solar panel micro-inverters, smart LED controllers and other Grid-connected devices. For easy and cost effective system design the *SM2480* combines N-PLC connectivity with analog signal monitoring and control required by such applications. As all the members of the *SM24xx* family, the *SM2480* features a dual core architecture, dedicating one core to the system control application and a second core to guarantee superior communication performance, while maintaining flexibility and programmability for OFDM based standards and proprietary communication schemes. Both cores are supported by full development systems to facilitate implementation of proprietary application specific control and monitoring algorithms.

Benefits

- Single-chip grid connected signal controller ideal for solar inverters, smart lighting ballasts and other SCADA applications reducing cost and simplifying the design
- Flexible high-speed PWM and high precision dual ADC allowing simultaneous sampling of I/V values as well as fast and flexible signal adjustments ideal for power-conversion applications
- Programmable comparator triggered events for fault detection and handling

- High speed, flexible and reliable communication through integrated programmable multi-mode N-PLC modem supporting all common OFDM standards including full compliance with: IEEE 1901.2, G3-PLC, PRIME, G.hnem as well as FSK/S-FSK and proprietary communication schemes
- Low latency communication schemes
- Cost optimized system solution with integrated A/D's, D/A's, OpAmp's, PGA
- Can operate as a stand-alone communications and analog controller device or in conjunction with external host MCU

Features

- Dual core architecture with custom N-PLC optimized DSP and Data Link Layer / Application 32bit controller
- High speed PWM with 6 channels of outputs with programmable pairing modes and independent settings
- 4 comparators with independent references and programmable fault detection
- 16 channel Signal Monitoring ADC with simultaneous sampling of voltage/current pairs
- High performing custom N-PLC DSP engine with embedded turnkey firmware featuring:
 - Configurable operational band within 5-500KHz range – compliant with CENELEC, FCC and ARIB bands operation
 - OFDM and FSK/S-FSK modulations
 - Compliant with IEEE 1901.2, PRIME, G3-PLC, CTIA/EIA709.2, G.hnem
 - Proprietary operation modes: NOFDM
 - Selectable differential and coherent BPSK, QPSK, 8PSK and coherent 16QAM modulations
 - Configurable data rate up to 500kbps
 - Programmable frequency notching to improve coexistence
 - Jammer cancellation
 - Adaptive tone mapping (on-off sub-band bit loading)

- FEC – Convolutional, Reed-Salomon and Viterbi coding
- CRC16
- Carrier RSSI, SNR and LQI indicators for best channel adaptation and L2/L3 metrics
- Zero-crossing detector
- Programmable 32bit RISC controller featuring:
 - Data Link Layer firmware options compliant with IEEE 1901.2, G3-PLC, PRIME, IEC61334-4-32 and others
 - Direct access to Signal Monitoring ADC for SCADA algorithms implementation - Solar micro-inverter, Arc detection, LED control, etc.
 - Carrier Sense Multiple Access/Collision Avoidance (CSMA/CA) channel access
 - Automatic Repeat Request (ARQ)
 - Meshing and self discovery mechanisms
- CCM* with AES128 / AES256 encryption core
- On-chip Peripheral Interfaces:
 - UART
 - 5 GPIO's
 - JTAG
 - SPI master for external flash
 - Up to 2 additional SPI slaves for metering, wireless transceiver or other devices
- Seamless interface to an external line driver for optimal system performance:
 - Integrated A/D and D/A
 - Integrated OpAmp's for RX and TX
 - Integrated PGA
- Low power operation modes
 - Offline mode
 - Listen mode
 - Receive mode
 - Transmit mode
- 3.3V (5V tolerant) digital I/O
- Receiver sensitivity of -80dBV

- -40 °C to +105 °C temperature range
- LQFP128pin package

Applications

- Solar micro-inverters and alternative energy management
- Smart lighting control
- Building automation (BA)
- SCADA (Supervisory Control And Data Acquisition)

Description

The *SM2480* is a highly integrated Communications and Signal Controller optimized for SCADA applications requiring communications and digital signal control, such as micro-inverters, LED controllers, etc.

The *SM2480* is single chip “grid connected” that combines the most advanced N-PLC connectivity with high speed PWM control logic and featuring extensive computational power to enable complete and flexible implementation of solar-microinverter, Arc detection, LED control and similar algorithms. The *SM2480* combines the benefits of programmable architecture with power and cost efficiency by utilizing two 32bit cores designed specifically for N-PLC modulations, voltage/current signal monitoring and M2M protocols.

To efficiently address applications, such as solar micro-inverters, the *SM2480* features a high speed flexible PWM controller with individually programmable 8 pairs of outputs with independent timing, and a number of analog interfaces that include 16 12-bit ADC channels and 4 comparators for high speed monitoring of I/V and other sensors.

As a member of the SM24xx family the *SM2480* features programmable OFDM based N-PLC modem including PHY, MAC and AFE featuring ADC, DAC, gain control and two OpAmp's for optimal system cost and performance.

The *SM2480* comes with a number of firmware versions implementing various N-PLC schemes, such as IEEE 1901.2, PRIME, G3-PLC, and other special modes tailored for SCADA and smart grid applications.

The *SM2480* enables secure communication with its 256-bit AES encryption core with CCM* mode support.

Typical Application Diagram

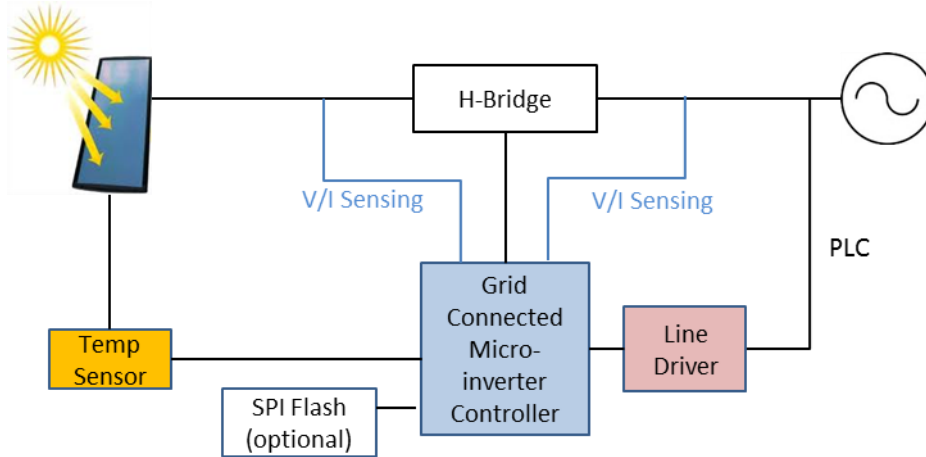


Figure 1 – SM2480 Based Grid Connected Solar Micro-inverter

Block Diagram

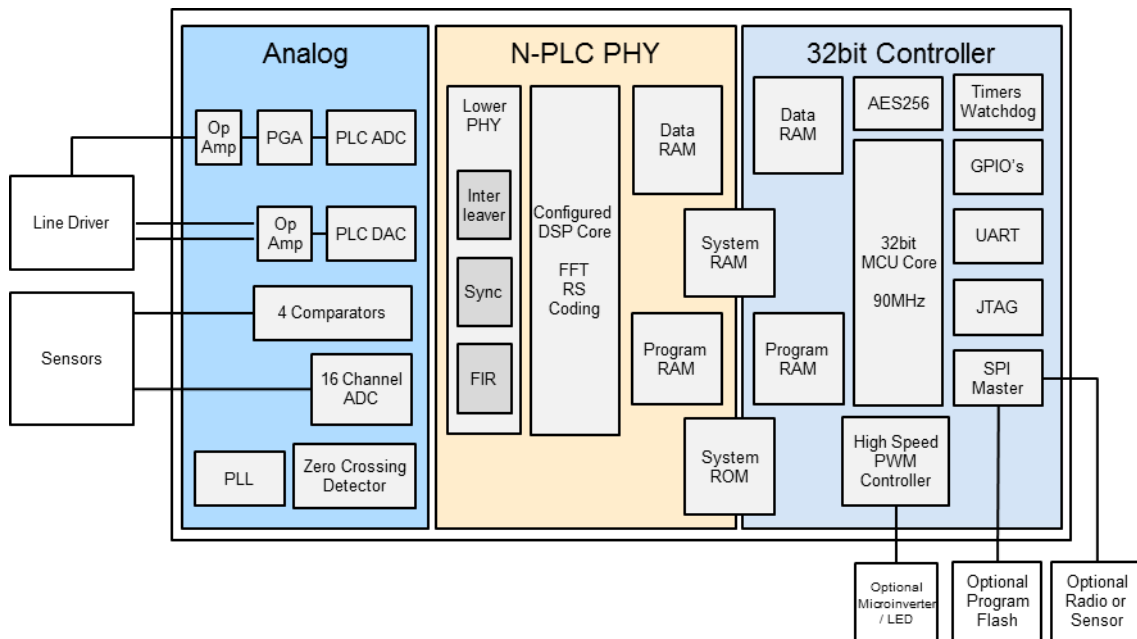


Figure 2 – SM2480 Block Diagram

Signal Control

Additionally to its high computational and DSP performance, the *SM2480* integrates advanced features necessary for implementing a complete analog control system. Those include High Speed PWM, Monitoring ADC, DAC and 4 analog comparators as well as triggering logic for fast fault detection.

High Speed PWM

The high speed PWM includes 12 channels and is designed to control analog peripherals with high short response time and high degree of accuracy such as power inverters and LED lighting.

Signal Monitoring Dual ADC

Signal monitoring is possible using the Simultaneous Sampling Dual ADC which is

designed to sample a multitude of sensors and in particular simultaneously sample current-voltage pairs. Conversions can be independently triggered by the PWM module or by a timer.

Timer Capture

Three timer capture modules are available for mains synchronisation and detection.

Analog Comparators & DAC's

The SM2480 incorporates four high-speed analog comparators with individual reference DAC's and individual multi-level hysteresis control.

N-PLC Modem

Both the SM2400 and the SM2480 support all common N-PLC standards in addition to several proprietary modes of operation. This enables maximum flexibility to the designer in implementing closed or grid connected SCADA systems utilizing analog control functions

Selectable Modes and Modulations

The SM2480 can be configured to operate in one of several modes, such as: 1901.2, G3-PLC, PRIME, ITU1901/2, G.hnem, S-FSK, NOFDM, etc. Different modes require different firmware images and imply different operational frequency bands with a varying number of carriers.

The SM2480 allows for configurable modulations per carrier. While most configurations are implied by the different standards, special modes can be created using specific combinations of carriers and modulations to achieve best performance in given channel conditions. The following modulations are available: Differential and coherent BPSK, QPSK and 8PSK and coherent 16QAM.

Forward Error Correction

The SM2480 supports Reed-Solomon (255,239) and (255,247), and rate half Convolutional coding with constraint length 7 (generator polynomial is [133,171]). In G3 and IEEE modes Convolutional coding is concatenated with RS to achieve the best reliability. Special error correction modes include extra repetition coding for increased robustness and puncturing for increased data rate on capable channels.

Communication Medium Metrics

The SM2480 has several metrics to assist in optimizing L2 and L3 channel adaptation and

routing. These metrics are RSSI, SNR and LQI, which is a measure of the data rate. The RSSI is an estimate of received signal strength. Each packet received can be interrogated for its estimated signal strength. This is very useful to determine the signal to noise ratio of different nodes on the network. It may be that the noise in a particular band is low but the signal is also attenuated significantly making data transmission unreliable. Network management systems can also interrogate each node for signal to noise ratios to create a database of all transmission path conditions. This produces a deterministic way of finding where repeaters are needed in a difficult environment even if they are dynamic.

Security

AES encryption engine conforms to FIPS 197 standard featuring CCM*, ECB, CBC, CTR modes and of up to 256 bit key size.

Zero-crossing Detector

The SM2480 has a zero-crossing input pin which takes signals generated by an external zero-crossing detector based on the transition through zero volts of a 50Hz (or 60Hz) sinusoidal on the power line. The SM2480 provides a phase detection feature allowing the transmission beginning at an arbitrary phase offset and measuring the phase offset of the received packet.

Analog Front End (AFE)

The SM2480 integrates an AFE optimized for N-PLC communication, which includes ADC, DAC, PGA and 2 OpAmp's to achieve the best signal power with minimum external BOM. External components include coupling circuitry and high voltage line driver that can vary for different applications and for different operational bands.

Peripheral Interfaces

The SM2480 includes several peripheral interfaces for adding optional components. Those interfaces include UART, SPI master for external flash interface and JTAG. The second SPI extends to two additional devices that can be used for telemetry or to interface to a wireless transceiver.

Boot Options

The SM2480 can be configured to boot in one of the following ways:

Boot Mode	MODE[2:0]	Description
SPI Master	"000"	Read and process valid bootsector from SPI Master SSb0
CI SPI Slave	"001"	Wait on Command Interface via SPI Slave
CI UART	"010"	Wait on Command Interface via UART
Parallel Memory	"011"	Read and process valid bootsector from Parallel Memory
Reserved	"1xx"	Reserved

Contact Information

For more information regarding the SM2480 including technical data sheets, application notes, sample enquiries, demonstration modules, pricing and ordering please contact:

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Version	Description	Date
0.1	First Draft	01/06/2014